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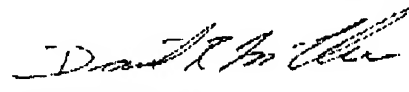
**PATENT**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of:	)	
	)	
Wang-Jin Chen	)	Confirmation No.: 9153
	)	
Serial No.: 10/733,095	)	Group Art Unit: 2825
	)	
Filed: December 11, 2003	)	Examiner: Rossoshek, Yelena
	)	
For: I/O CIRCUIT PLACEMENT METHOD AND	)	TKHR Ref. 250606-1020
SEMICONDUCTIVE DEVICE	)	Top-Team ref. 0697-10333US
	)	
	)	
	)	

**CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR §1.8**

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted on the date indicated below via facsimile to the United States Patent and Trademark Office, Technology Group 2100, facsimile number (571) 273-8300. Total number of pages in this transmission 9.

March 5, 2006  
Date

  
Daniel R. McClure

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Mail Stop - Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

The Office Action mailed, January 11, 2006 has been carefully considered. In response thereto, Applicant respectfully requests consideration of the following remarks.